1 Scope

The present specifications shall apply to Sanken silicon diode, RM1C.

2 Outline

Туре	Silicon Rectifier Diode (Mesa type)				
Structure	Resin Molded Flammability: UL94V-0 (Equivalent)				
Applications	Commercial Frequency Rectification, etc				

3 Absolute maximum ratings

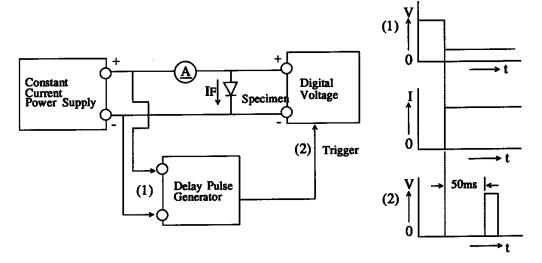
	Transient PetekuReverse Voltage	SymW _R J _M	UnitV	Rating	Conditions
1	Transikir Reventse Revolutinge Voltage	V_{RMRM}	V V	1050	
2	PeakveragerForModuag€urrent	V _{IIM(AV)}	V A	1000	
3	Av PeakeStrogevatrok QurdeQu rrent	I _{F(A} V _{SM}	A A	0.8	Refer to derating curve in Section 6
4	Peak Surge Forward Current	I_{FSM}	A	40	10ms. Half sine wave, one shot
5	Junction Temperature	T_j	°C	-40 to +150	
6	Storage Temperature	T_{stg}	°C	-40 to +150	

4 Electrical characteristics (Ta=25°C, unless otherwise specified)

No.	Item	Symbol	Unit	Rating	Conditions
1	Forward Voltage Drop	V_{F}	V	1.2 max.	I _F =1.0A
2	Reverse Leakage Current	I_R	μΑ	5 max.	$V_R = V_{RM}$
3	Reverse Leakage Current Under High Temperature	$H \cdot I_R$	μΑ	100 max.	$V_R=V_{RM}, T_j=100^{\circ}C$
4	Thermal Resistance	$R_{\text{th(j-l)}}$	°C/W	15 max.	Between Junction and Lead

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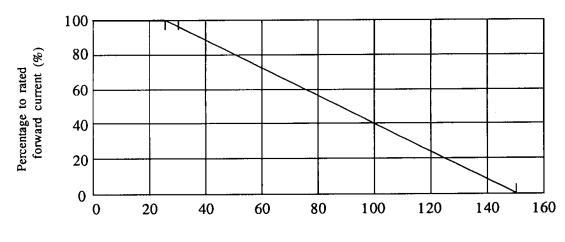
$5~V_F$ test and test circuit



6 Derating

Derating to the ambient temperature.

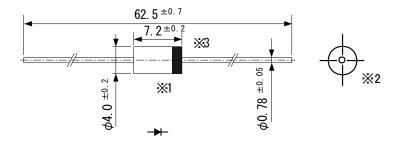
Power loss generated by voltage is not taken into consideration.



Ambient Temperature (°C)

7 Package information

7-1Package type, physical dimensions and material



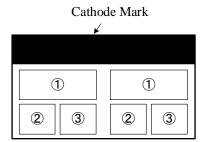
- *1 The allowance position of Body against the center of whole lead wire is 0.5mm(max.)
- *2 The centric allowance of lead wire against center of physical body is 0.2mm(max.)
- *3 The burr may exit up to 2mm from the body of lead

Dimensions in mm

7-2 Appearance

The body shall be clean and shall not bear any stain, rust or flaw.

7-3 Marking



- ① Type number RM1C

From 1 to 9 for Jan. to Sep.
O for Oct., N for Nov., and D for Dec.

- ③ Lot number 2 (ten days)
 - · Top of the month
 - · · Middle of month
 - · · · End of month

The type No. and Lot. No. are to be marked in white.